A critical review of power quality standards and definitions applied to DC microgrids

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HIGHLIGHTS

• A review of the power quality standards to verify the applicability to DC microgrids.
• An encompassing overview of the power quality issues in DC microgrids.
• A simulation example to exemplify the less conceivable DC power quality issues.

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ABSTRACT

As compared to AC microgrids, DC microgrids reduce the hardware complexity of converter-dominated power distribution in the presence of a high number of renewable energy sources, energy storage systems and energy efficient loads. Another frequently highlighted advantage is their resiliency and tolerance against AC grid disturbances resulting in improved overall power quality. However, with respect to power quality, the question arises whether existing international power quality standards and metrics remain applicable in DC microgrids or require adjustments. Therefore, this paper critically revises the definitions and power quality indicators specified in IEC 61000 and IEEE Std1159. The resulting review is essential to unambiguously define the responsibilities of the microgrid operators, customers and device manufacturers. Apart from that, causes and consequences of power quality issues in DC microgrids are discussed.

1. Introduction

Considering the increasing number of distributed generation, electric vehicles, energy storage and energy efficient loads that inherently generate or consume DC power, DC microgrids () offer a promising alternative to the AC counterpart [1]. The improved compatibility between the DC devices and a DC power backbone reduces and simplifies the power conversion steps, thereby reducing the power conversion losses and increasing the component-level reliability [1]. Efficiency gains in the range of 12–18% have been reported [2]. Apart from the improved compatibility, enable to transfer more power per conductor cross-section [3] and allow to actively control the device currents by means of the switch-mode DC-DC converters that interface the DC devices with the network. Applications considered are electric vehicle charging stations [4] and other charging applications [5].

Increased power quality is often highlighted as a distinguishing feature of DC microgrids [6]. Because the DC voltage is tightly controlled, in part by AC/DC converters connected to an overlay AC grid, the is more tolerant against AC side disturbances [7]. Power quality is thus highly intertwined with voltage stability, as the DC voltage is a metric for the power balance in the system and needs active stabilization [8]. But it is unlikely that high levels of power quality remain assured when multi-vendor will develop, each adhering to their own guidelines and requirements. Standardization is frequently mentioned as a hurdle for the wide-scale deployment of [9]. An important part of standardization encompasses power quality standards and metrics, required for ensuring interoperability and assigning quantifiable responsibilities.

Available literature regarding power quality in primarily focuses on solutions addressing specific power quality issues. A first solution focuses on reducing voltage oscillations by implementing active damping [10]. Alternatively, voltage oscillations are reduced by implementing nonlinear controllers [11]. Another solution applies converter interleaving to reduce the voltage ripple in the network [12]. Nuutinen et al. have investigated common-mode and radio-frequency electromagnetic interference due to pulse-width modulated inverters in a public

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distribution low-voltage DC (LVDC) research site [13]. Although the proposed solutions definitely enhance the power quality of, it remains unclear what is required from the system-level perspective.

From the system-level point-of-view, Magro et al. proposed DC power quality indices for low-frequency sinusoidal disturbances and voltage ripple [14,15]. Whaite et al. have addressed interharmonics and the impact of fault and inrush currents on the voltage in [16]. However, there remains a need to critically review whether existing power quality standards are applicable to or require adjustments.

Therefore, the present work serves as a starting point by revisiting the current international power quality standards IEC 61000 and IEEE Std1159. The paper thereby excludes radiated phenomena, electrostatic discharge and nuclear electromagnetic pulse as classified by IEC 61000 [17]. The aim is to revise the current power quality framework and verify its applicability to, targeting all stakeholders that should address particular issues listed in this paper.

This paper is structured as follows: the next section revisits the concept of power quality as defined by different international standards and verifies the applicability of the definitions to. The third section presents a classification of DC power quality issues, its causes and its consequences. As some power quality issues in DC are less understandable, the fourth section will present simulation results as an example. The last section concludes this paper.

2. Power quality definitions

The first concern when dealing with power quality is the availability of a proper definition that is sufficiently general to encompass both AC and DC power quality. The definition provided by the IEC 61000 is suited in that sense as it makes no distinction between AC and DC power quality when stating that "Power quality encompasses the characteristics of the electricity at a given point on an electrical system, evaluated against a set of reference technical parameters" [17]. Other references provide different definitions for power quality, but none of them makes a distinction between AC and DC power quality:

- Power quality is the combination of voltage quality and current quality. Thus power quality is concerned with deviations of voltage and/or current from the ideal waveform [18].
- Any power problem manifested in voltage, current, or frequency deviations that results in failure or misoperation of customer equipment [19].
- Power quality is the concept of powering and grounding sensitive equipment in a matter that is suitable to the operation of that equipment and compatible with the premise wiring system and other connected equipment [20].

On the other hand, the IEEE recommended practice Std1709 addresses 1–35 kV medium-voltage DC (MVDC) distribution systems and defines power quality as “compliance with specified voltage tolerances and voltage ripple” [21]. That definition clearly narrows down power quality to voltage quality, which is a fundamental characteristic of DC power distribution systems as the DC voltage is a distributed measure of the power balance in DC power distribution systems.

3. DC power quality issues

Fig. 1 classifies the power quality issues that are conceivable within DC distribution systems based upon the duration of the phenomenon (time axis) and the frequency content of the waveform (frequency axis). The time axis is subdivided into short-duration, transient phenomena and long-duration, steady-state phenomena. The frequency axis in turn is subdivided into high-frequency components (typically in the range of the switching frequency 1 kHz and above), components in the order of magnitude of the control bandwidth of the voltage controllers present within the and in DC components. The power quality issues that will be subsequently discussed are also summarized in Table 1, including the indicators to quantify them, possible causes and consequences.

3.1. Transients

According to the IEEE Std1159 [22], impulsive transients are sudden, nonpower frequency changes from the nominal condition of voltage, current, or both that are unidirectional in polarity. Accordingly, oscillatory transients are sudden, nonpower frequency changes in the steady-state condition of voltage, current, or both, that include both positive and negative polarity values. The impulsive transients are characterized by their rise time, fall time and peak magnitude, while oscillatory transients are characterized by their frequency spectrum and duration. In IEEE Std1159 [22], low-frequency (< 5 kHz), medium frequency (5–500 kHz) and high-frequency (0.5–5 MHz) oscillatory transients are distinguished. Similarly, the EN50160 [24] defines transient overvoltage as a short duration oscillatory or non-oscillatory overvoltage usually highly damped and with a duration of a few milliseconds or less. According to IEC61000 (Annex A.3) [17] a transient designates “a phenomenon or a quantity which varies between two consecutive steady states during a time interval short when compared with the timescale of interest”. The proposed definitions are equally applicable to. Impulsive and oscillatory transients in are possibly caused by lightning strikes or sudden switching operations causing induced voltages. They typically occur beyond the frequency range of the controller bandwidth. This may cause insulation breakdown or trigger system resonances.

3.2. Short- and long-duration variations

Voltage variations in may be categorized based upon their duration, magnitude and direction of change, similar to the IEEE Std1159 [22]. Short-term and long-term variations are distinguished, primarily based upon the underlying causes. Short-term variations are caused by switching operations, faults and large power fluctuations on the system, while long-term variations occur in steady-state due to power flow variations. An arbitrary boundary of one minute is used in practice for AC to distinguish both and a further categorization in terms of instantaneous, momentary and temporary variations is made [22].

Based on the direction of change and their magnitude, three rms voltage variations may occur: sags, swells and interruptions. According to IEEE Std1159 [22], a sag is defined as “a decrease in rms voltage between 0.1 and 0.9 p.u. for durations from 0.5 cycles up to 1 min”. A
Table 1
DC power quality issues (based upon [22,23]).

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swell is defined as “an increase in rms voltage above 1.1 p.u for durations from 0.5 cycles up to 1 min”. An interruption is defined as “a decrease in rms voltage below 0.1 p.u. for durations from 0.5 cycles and 1 min”. For durations longer than 1 min, the IEEE Std1159 [22] defines the subcategories undervoltage, overvoltage and sustained interruption, i.e. the long-duration variants of sags, swells and interruptions. The same magnitude levels apply as for short-duration variations.

While the IEEE Std1159 [22] include rms in the respective definitions, the IEC61000 [17] does not. A voltage dip (synonym of sag) is “a temporary reduction of the voltage at a point in the electrical system below a threshold”. A voltage swell is defined as “a temporary increase of the voltage at a point in the electrical system above a threshold”. Interruption and voltage dip share the same definition, although the threshold level for an interruption is lower. The IEC61000 standard does not define the concepts of over- and undervoltage as the IEEE Std1159 does for steady-state deviations.

The ENS0160 [24] adopts similar definitions, but specifies the 0.9 and 1.1 p.u. threshold levels for voltage dips and swells respectively. A supply interruption is defined as “a condition in which the voltage at the supply terminals is lower than 5% of the reference voltage”. Furthermore, it sets the boundary between short- and long-term interruptions at 3 min. Note that the presented definitions are also applicable to DC power distribution systems without reformulation, although the threshold levels may be a subject for further discussion. The Finish SFS-
6000 standard for instance limits the DC voltage in the −25% to +10% range relative to its nominal value [25].

Voltage sags and swells in are caused by capacitor switching as the front-end of the dc-dc converters in the network generally contain filter capacitors, equipment turn-on and turn-off and faults. Interruptions occur primarily in a disconnected section following a fault. Similarly, under- and overvoltages are caused by power variations or as a consequence of poor voltage regulation. Voltage swells and overvoltages are considered to have a more detrimental impact on the equipment operation as system infrastructure (dc-dc converters, cable infrastructure) may experience irreversible insulation breakdown. On the contrary, voltage sags and undervoltage may trigger devices to shut-down and disconnect, which has an impact on the system’s availability. Furthermore, increased losses may occur in case of undervoltage, because the current levels increase to distribute the same amount of power. Another consequence of undervoltage is voltage instability in the presence of constant power loads, as the stability margins decrease [11].

When considering bipolar DC distribution networks, that adopt a three-wire (positive-neutral-negative) configuration [26,27], a voltage sag or swell can also be the consequence of a load decrease or increase respectively, opposite to what one would expect. Voltage sags or undervoltage may trigger devices to shut down and disconnect, which has an impact on the system’s availability. Furthermore, increased losses may occur in case of undervoltage, because the current levels increase to distribute the same amount of power. Another consequence of undervoltage is voltage instability in the presence of constant power loads, as the stability margins decrease [11].

Voltage sag causes a sudden drop in voltage, which can lead to a temporary loss of power. Swells, on the other hand, are characterized by a sudden increase in voltage. These phenomena can be caused by various factors, including equipment turn-on and turn-off, faults, and power variations. Interruptions are caused by the loss of power supply, which can be triggered by a fault or a disconnection of a section of the network.

3.3. Imbalance

3.3.1. Voltage imbalance

In three-phase AC systems, voltage imbalance is characterized by the voltage unbalance factor (i.e. the ratio between the negative- or zero-sequence and positive-sequence voltage) which is limited to 2% in EN50160 [24] and 3% in IEEE Std1159 [22]. The IEC61000 [17] adopts the same definitions.

In bipolar, a similar phenomenon may occur in case devices are connected between the poles and neutral conductor, similar to single-phase devices in three-phase AC systems. Alternatively, voltage imbalance may be caused by pole-to-neutral faults.

The decomposition into balanced and unbalanced components of voltages and currents in bipolar [28,27] enables to define a voltage unbalance factor $VUF$ in (1) as the ratio between the unbalanced and balanced voltage at a certain node in the network.

$$VUF \equiv \frac{V_p - V_n}{V_p + V_n}$$

where $V_p$ is the positive pole-to-neutral voltage, $V_n$ is the neutral-to-negative pole voltage, $V_U \equiv \frac{V_p - V_n}{2}$ is the unbalanced voltage and $V_B \equiv \frac{V_p + V_n}{2}$ is the balanced voltage.

For three-phase AC constraining the $VUF$ is essential in the presence of three-phase AC machines without active front-end as negative sequence components increase the thermal loading of the machine [29]. However, in bipolar, devices function correctly as long as the pole-to-neutral voltage constraints are respected as defined in the short- and long-duration variations. A specific $VUF$ constraint thus becomes obsolete.

3.3.2. Current imbalance

Current imbalance causes currents in the neutral conductor, potentially overloading it and triggering overcurrent protective devices. Parallel to the definition of balanced and unbalanced voltages, current imbalance in the network cable can be characterized by the unbalanced current $I_U$ in (2).
\[ I_V \equiv \frac{I_p - I_n}{2} \]  

(2)

where \( I_p \) is the current drawn from the positive pole by a device connected between the positive pole and the neutral and \( I_n \) is the current drawn from the negative pole by a device connected between the neutral and negative pole.

\( I_n \) is related to the neutral conductor current, depending on the network configuration. For example, in the case of a radial network, \( I_n \) represents twice the neutral conductor current. Attention should be paid in case a net amount of current is injected in the positive pole and withdrawn from the negative pole and vice versa. In that condition, up to twice the nominal pole conductor current may flow in the neutral conductor.

3.4. Waveform distortion

3.4.1. AC offset

Power frequency AC voltage waveforms can appear superimposed on the DC voltage level due to unintended galvanic or magnetic coupling between the AC and the DC [30]. While DC offset in AC may cause transformer saturation and associated heating and stressing of the insulation [22], an AC offset in leads to reactive currents in the network causing additional losses and possibly intolerable voltage variations.

The amplitude of the power frequency component (50 or 60 Hz) relative to the fundamental DC voltage component is a measure to quantify AC offset in. As long as the magnitude remains limited to the voltage constraints specified for short- and long-duration variations, the AC power frequency component is likely to be filtered by the DC-DC point-of-load converters regulating the output voltage at the end-device hence causing no loss of operation. However increased losses in the distribution system remain a motivation to impose a limitation.

3.4.2. Interharmonics and noise

As harmonics are defined as “sinusoidal waveforms having frequencies that are integer multiples of the power frequency” [22], harmonics by definition do not exist in DC distribution systems. All distinct sinusoidal waveforms that may occur in are consequently classified as interharmonics, i.e. not integer multiples of the power frequency [22]. Interharmonics may be caused by switch-mode converters that inject current at their respective switching frequency and harmonic components of the switching frequency due to pulse-width modulation. Interharmonics cause additional losses in the filter capacitors and may disturb communication signals and measurement devices [31]. For example, interharmonics may impact the proper operation of arc-fault detection devices and residual current devices. Moreover, interharmonics increase the conduction losses.

While interharmonics in AC occur generally at frequencies in the range of the power frequency, interharmonics in DC may also occur at significantly higher frequencies near the switching frequency in the 1–100 kHz range and higher with the advent of wide-bandgap power semiconductors [32,33]. Hence, the distinction between noise (spectral content typically lower than 200 kHz) becomes less strict. As also the consequences of noise and (switching) interharmonics coincide, a distinction between both phenomena may have no further relevance. A recent example where interharmonics appear are bipolar DC networks powered by a neutral-point clamped inverter [34].

Mariscotti et al. recognized that interharmonics are closely related to voltage and current ripple, which they propose to quantify using the ripple index as defined in (3) for a sampled voltage or current signal \( q[n] \) [14,15].

\[ q_{\text{pp},T} = \max_{n,k} (q[n] - q[n + k + k]) \]  

(3)

where \( q_{\text{pp},T} \) equals the peak-to-peak ripple index evaluated over the time window \( T \). \( q[n] \) is the sampled voltage or current signal with index \( n \), \( k \) is a positive integer, \( k_r = \frac{T}{\Delta f} \) and \( \Delta f \) is the sample period.

It has been shown that the peak-to-peak ripple index can be evaluated using analog filtering or a discrete fourier transform [15]. Furthermore, the authors propose to evaluate the peak-to-peak ripple for three different time windows, namely \( T = 6.7 \mu s \), \( T = 50 \mu s \) and \( T = 500 \mu s \) to account for different ripple frequencies in the 2 kHz and 150 kHz range, caused by large and small power electronic converters respectively.

Alternatively, the relative peak-to-peak ripple may be defined as in [35].

\[ q_{\text{pp},T} = \frac{q[n_{\text{max}}] - q[n_{\text{min}}]}{q_{\text{dc}}} \]  

(4)

where \( q[n_{\text{max}}] \) and \( q[n_{\text{min}}] \) are respectively the maximum and minimum values of the sampled signal \( q[n] \) observed over the time window \( T \) and \( q_{\text{dc}} \) is the DC value of the sampled signal over the time window \( T \).

3.4.3. Notching

IEEE Std1159 defines voltage notching as “a periodic voltage disturbance caused by the normal operation of power electronic devices when current is commutated from one phase to another” [22]. Alternatively, IEEE Std519 defines a notch as “a switching (or other) disturbance in the normal voltage waveform, lasting less than 0.5 cycles, which is initially of opposite polarity than the waveform and is thus subtracted from the normal waveform in terms of the peak value of the disturbance voltage” [31]. Clearly, those definitions are based upon the underlying cause of voltage notches in AC, namely the commutation from one phase to another in power electronic converters [22,36]. Voltage notches in AC have been reported to excite the natural frequencies of the system and to cause parallel resonances, potentially causing overvoltage stress [36]. Compatibility levels for AC have been established in IEC61000-2-2 [37].

Notching is characterized by the notch depth and the duration. It is caused by switch-mode dc-dc converters with insufficient capacitance at the front-end as will be exemplified in the next section.

3.5. Voltage fluctuations

Voltage fluctuations in AC distribution systems lead to low-frequency light intensity oscillations that discomfort human beings, a phenomenon referred to as flicker [22]. IEC61000 defines flicker as “Impression of unsteadiness of visual sensation induced by a light stimulus whose luminance of spectral distribution fluctuates with time” [17]. On the other hand, voltage fluctuations may cause periodic peak voltages that outreach the tolerable voltage range.

The voltage fluctuations result from low-frequency power fluctuations, subsequently causing DC voltage fluctuations. Similarly, DC voltage fluctuations may also result from providing reactive power and other ancillary services to the overlay AC grid [38].

In AC, voltage fluctuations are quantified using the short-term and long-term flicker severity index, which quantifies how flicker is perceived by human beings through a series of sensitivity functions originating from a rms voltage measurement [39]. The sensitivity functions have been specifically established for incandescent light bulbs and hence are not applicable to other lighting sources such as LED or fluorescent lighting.

The question arises whether these voltage fluctuations will have noticeable impact as lighting fixtures are increasingly converter-interfaced, a discussion which is both relevant in AC and DC. For example, it has been concluded that led drivers equipped with DC-DC converters, that control the output current to maintain the luminous flux, show higher immunity to superimposed AC voltage variations [40]. Therefore the practical relevance of voltage flicker is subject for debate, both in AC and DC.
3.6. AC power quality issues not present in DC

The previous discussion has shown that the power quality issues, originally defined from an AC perspective, are also relevant in DC. However, harmonics [21] and power frequency variations disappear in the DC context.

3.7. Summary

To summarize this section, consider Table 2, which indicates for each power quality issue:

- Whether the issue appears in (✓) or not (□).
- Whether a definition is available and appropriate for (✓), is undefined (□), or requires reconsideration (Δ).
- Whether an indicator is available and appropriate for (✓), is undefined (□), or requires reconsideration (Δ).

4. Simulation results

Some power quality issues, their causes and consequences are less clear-cut and will therefore be illustrated in the simulation of a half-bridge and neutral-point clamps (NPC) converter depicted in Fig. 2. The half-bridge converter is supplied from a unipolar voltage source \( v_i = 400 \text{ V} \) via the line resistance \( R_L = 0.1 \Omega \) and drives a constant current load of \( I_L = 100 \text{ A} \). The converter operates at a constant switching frequency of 16 kHz. The NPC also operates at 16 kHz and connects a bipolar DC system to an overlay AC grid.

The first simulation case shown in Fig. 3a considers the load turn-on (at \( t = 50 \text{ ms} \)) and turn-off (at \( t = 250 \text{ ms} \)) dynamics. The half-bridge operates at 50% fixed duty cycle and step-changes of \( I_L = 0 \rightarrow 100 \rightarrow 0 \text{ A} \) are applied. The figure depicts the resulting voltage transient at the DC bus, showing that the current step-changes trigger the LC resonance of the output filter, which are also observed at the front-end capacitor \( C_1 \). Hence, the turn-on and turn-off events cause oscillatory transients at 225 Hz with a magnitude of 5 V. Secondly, if the load current exceeds a certain threshold, it may cause a permanent undervoltage in the network.

The second simulation case shown in Fig. 3b considers a 100 Hz fluctuating constant load \( I_L \) with an average value of 100 A and an amplitude of 20 A. These power fluctuations cause voltage fluctuations across \( C_1 \), as depicted in the figure. Higher DC bus capacitors reduce the magnitude of the voltage fluctuations. The voltage waveform also includes switching ripple of 16 kHz, which can be considered as interharmonics as it has a discrete frequency spectrum. Of course, when multiple of these half-bridge converters would operate in parallel at different switching frequencies, the voltage ripple will have a continuous spectrum. The peak-peak voltage ripple in this case equals \( q_{pp} = 0.56 \% \) according to (3) and \( q_{pp} = 0.37 \% \) according to (4).

The third simulation case shown in Fig. 3c zooms into three switching periods (the switching period equals 62.5 μs) and shows the voltage ripple in case the load current amounts 100 A continuously for two different capacitance values of \( C_1 = 20 \mu \text{F} \) and \( C_1 = 1 \text{ mF} \). It demonstrates that a relatively small capacitance results into notches in the voltage waveform of 10 V, corresponding to RC transient waveforms of repetitively charging and discharging \( C_1 \).

The fourth simulation case considers the NPC in Fig. 2b, interfacing a 230 V 50 Hz AC system to a ±350 V bipolar DC system. As shown, the bipolar DC system contains two constant current sources connected between the positive-to-neutral and negative-to-neutrally respectively. Fig. 3d depicts the simulation results for the two bus voltage \( v_p \) and \( v_n \) and both current sources set at 10 A. The results exhibit a start-up transient and at \( t = 50 \text{ ms} \) a step-change is applied to \( I_L \rightarrow 10 \rightarrow 14 \text{ A} \). The simulation results present two DC power quality issues, namely imbalance and 150 Hz interharmonics.

5. Conclusions

This paper critically reviewed international power quality standards IEEE Std1159 and IEC 61000 in the light of DC microgrids. It is concluded that the majority of existing definitions of power quality are sufficiently general to encompass DC microgrids. Voltage transients, short- and long-term variations, noise, notching and voltage fluctuations are highly similar to the existing AC power quality issues. However the power quality issues AC offset, interharmonics and imbalance are clearly different. Furthermore, harmonics, DC offset and power frequency variations are by definition not present in DC microgrids. In bipolar DC microgrids voltage imbalance may occur and a voltage unbalance factor has been defined. However, a specific limit is not necessary, as other voltage constraints already ensure the functionality. The paper finally illustrated by simulation that power electronic converters can cause certain power quality issues which can be mitigated by appropriate filtering at the DC front-end. In general, it is anticipated that power quality issues in DC microgrids shift to higher frequencies as compared to AC because of the switch-mode operation of power electronic converters, the bandwidth of the controllers and the rapid fault dynamics.

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References

[21] IEEE recommended practice for 1 kV to 35 kV medium-voltage DC power systems on ships; 2010, doi:https://doi.org/10.1109/IEEESTD.2010.5623440.
[31] IEEE. S19-2014 IEEE recommended practice and requirements for harmonic control in electric power systems; 2014.